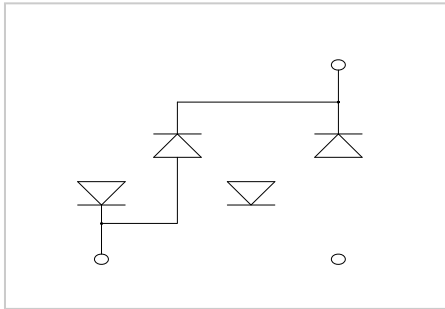




Bridge Rectifiers

Tin plated leads, solderable per
J-STD-002 and JESD22-B102



- **Polarity:** As marked on body

■ Maximum Ratings ($T_a=25$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	MBLSK22S	MBLSK24S	MBLSK26S	MBLSK28S	MBLSK210S	MBLSK215S	MBLSK220S
Device marking code			MBLSK22S	MBLSK24S	MBLSK26S	MBLSK28S	MBLSK210S	MBLSK215S	MBLSK220S
Repetitive peak reverse voltage	VRRM	V	20	40	60	80	100	150	200
Average rectified output current @60Hz Half-sine wave, Resistance load, TL (FIG.1)	I _O	A	2.0						
Surge(non-repetitive)forward current @ 60Hz half-sine wave,1 cycle, T _j =25	I _{FSM}	A	50						
Current squared time @1ms≤t≤8.3ms T _j =25									

	S Y M B O L	U N I T	T E S T C O N D I T I O N	M B L S K 2 2 S	M B L S K 2 4 S	M B L S K 2 6 S	M B L S K 2 8 S	M B L S K 2 1 0 S	M B L S K 2 1 5 S	M B L S K 2 2 0 S
Maximum instantaneous forward voltage drop per diode	V _F	V	I _{FM} =1.0A	0.50		0.70	0.85		0.90	
Maximum DC reverse current at rated DC blocking voltage per diode@ V _{RM} =V _{RRM}	I _{RRM}	uA	T _a =25	500			100			
			T _a =100	10000			5000			



MBLSK22S THRU MBLSK220S

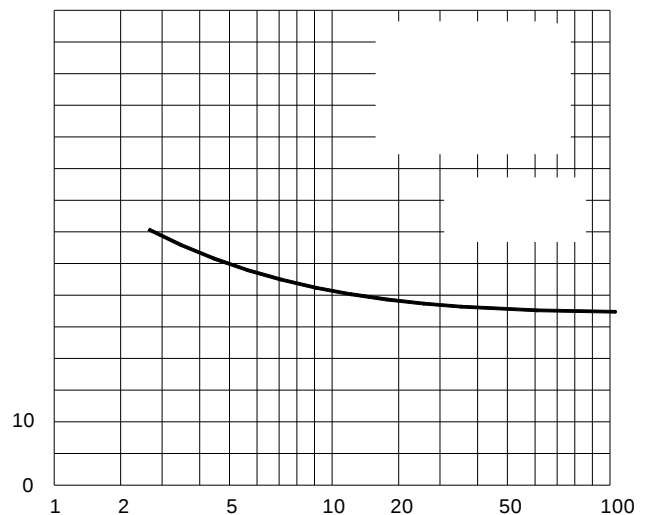
■ Thermal Characteristics $T_a=25$ Unless otherwise specified

PARAMETER		SYMBOL	UNIT	MBLSK22S	MBLSK24S	MBLSK26S	MBLSK28S	MBLSK210S	MBLSK215S	MBLSK220S
Thermal Resistance	Between junction and ambient, On alumina substrate	R θ J-A	/W	76.0						
	Between junction and ambient, On glass-epoxy substrate	R θ J-A		134.0						
	Between junction and lead	R θ J-L		20.0						

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
MBLSK22S-MBLSK220S	F1	Approximate 0.083	4000	8000	64000	13' reel
MBLSK22S-MBLSK220S	F3	Approximate 0.083	5000	10000	80000	13' reel

■ Characteristics(Typical)





MBLSK22S THRU MBLSK220S

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